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which can help you to get all. Free Download Winamp Serial Download. The present invention is generally directed to the art of semiconductor circuits. More particularly, the present invention is directed to a system and method of creating and using an output spacer on a semiconductor device. Merely by way of example, the present invention is applied to the process of forming a trench oxide of an eDRAM (embedded dynamic random access

memory) chip. However, it will be recognized that the invention has a much broader range of applicability. For example, the invention can be applied to the formation of other MOS devices, the formation of polysilicon for CMOS devices, the formation of poly-Si:a-Si-silicide devices, oxidation and/or others. Integrated circuits have evolved from a handful of interconnected devices fabricated on a single chip of silicon to

millions of devices. Conventional integrated circuits provide performance and complexity far beyond what was originally imagined. In order to achieve improvements in complexity and circuit density (i.e., the number of devices capable of being packed onto a given chip area), the size of the smallest device feature, also known as the device "geometry", has become smaller with each generation of integrated circuits.

Semiconductor devices are now being fabricated with features less than a quarter of a micron across. Increasing circuit density has not only improved the complexity and performance of integrated circuits but has also provided lower cost parts to the consumer. An integrated circuit or chip fabrication facility can cost hundreds of millions, or even billions, of dollars. Each fabrication facility will have a certain

throughput of wafers, and each wafer will have a certain number of integrated circuits on it. Therefore, by making the individual devices of an integrated circuit smaller, more devices may be fabricated on each wafer, thus increasing the output of the fabrication facility. Making devices smaller is very challenging, as each process used in integrated fabrication has a limit. That is to say, a given process typically only works down

to a certain feature size, and then either the process or the device layout needs to be changed. Additionally, as devices require faster and faster designs

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